



PATENT #8
Docket No. 325772009100

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Kenji MASAKI

Serial No.: 09/287,530

Filing Date: April 7, 1999

For: DIGITAL IMAGE PROCESSING
APPARATUS

Examiner: Choobin Mahmood

Group Art Unit: 2621

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REQUEST FOR RECONSIDERATION UNDER 37 CFR 1.116

BOX AF
Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the final Office Action dated February 26, 2002, please consider the following remarks.

REMARKS

Claims 1-15 stand rejected under 35 USC 102(b) as being anticipated by Yoshida. This rejection is respectfully traversed. Independent claims 1, 6 and 11 all include placing bits for describing information in "**specific bit positions** of pixel data . . . at predetermined positions of said processed image." For Example, applicants describe one specific bit position as the least significant bit (LSB) position of intensity data of each pixel. See page 12, lines 14-16.

Yoshida does not describe placing information in specific bit positions of pixel data as claimed by applicants. The Examiner states that FIG. 6 in Yoshida shows the placement of bits